

FIG. 1

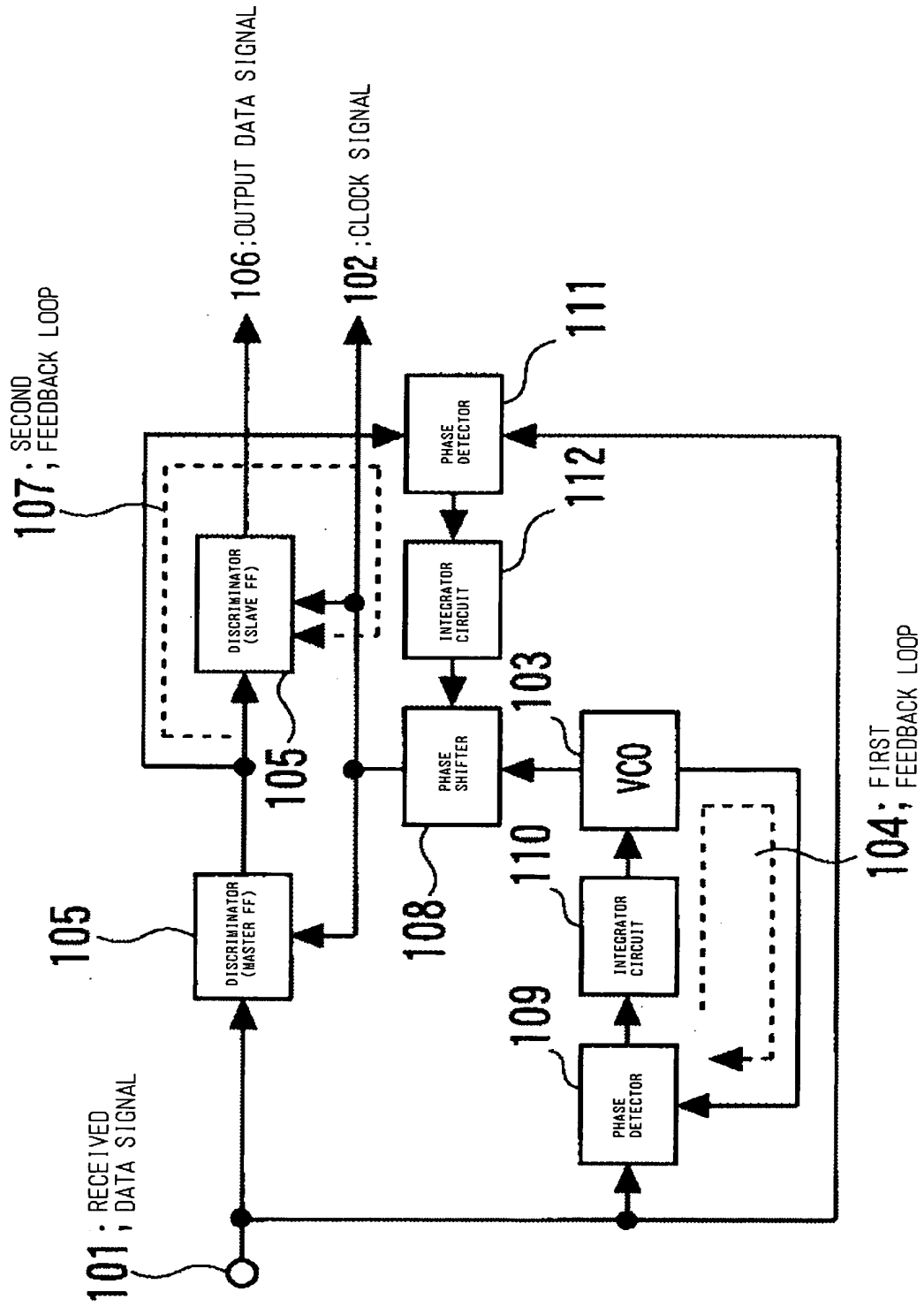


FIG. 2

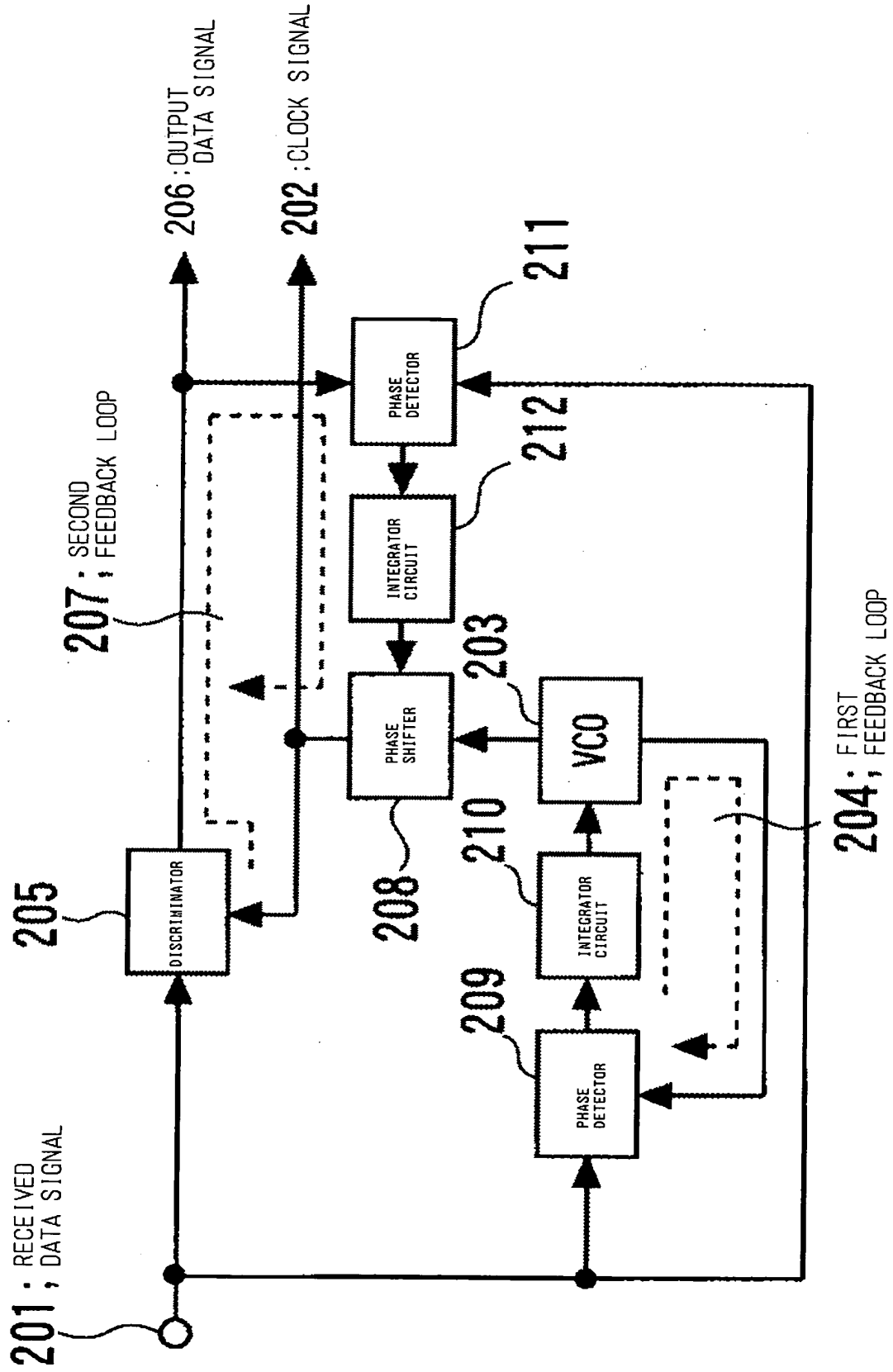


FIG. 3

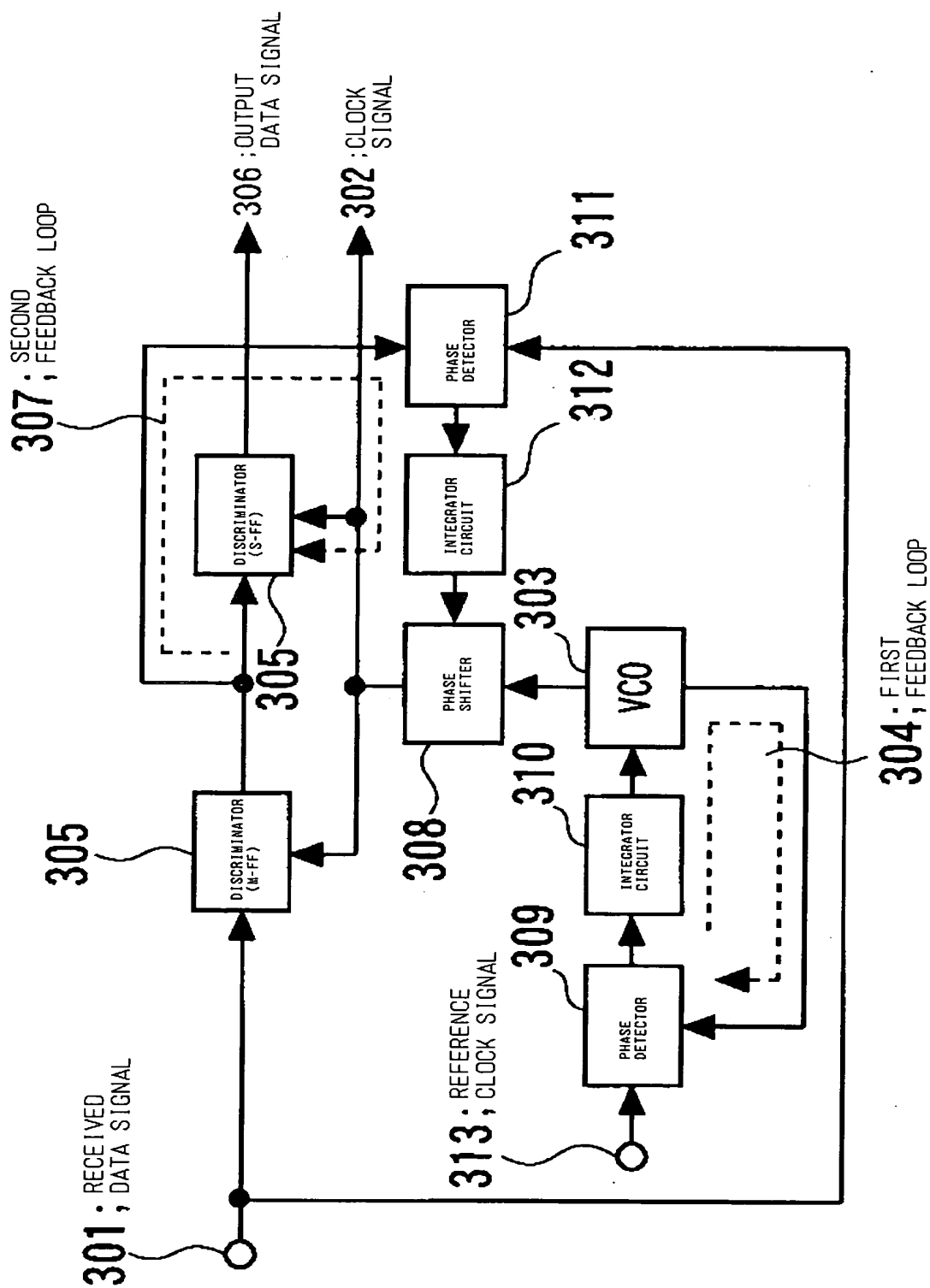


FIG. 4

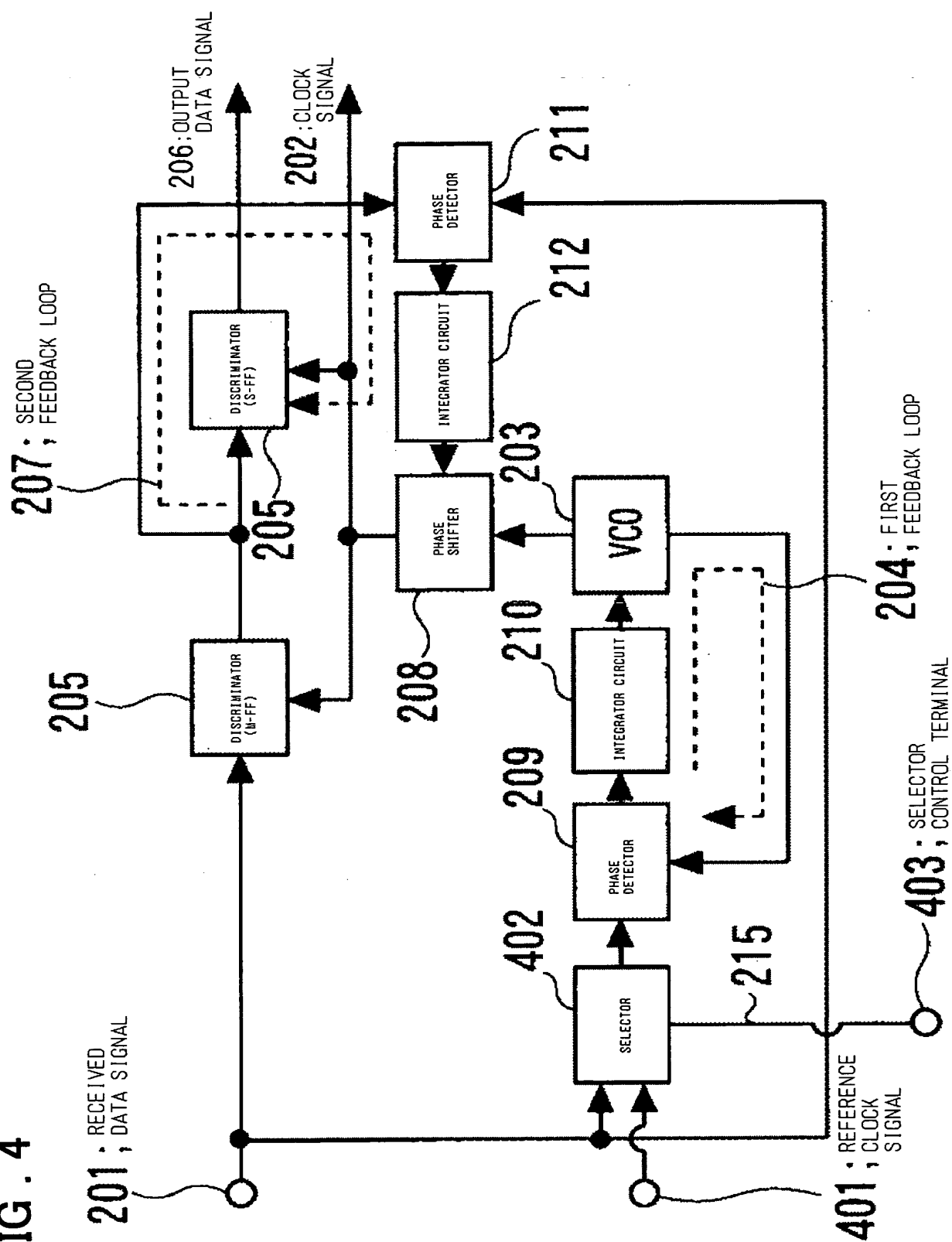


FIG. 5A CASE OF PHASE LEAD OF RECEIVED DATA SIGNAL

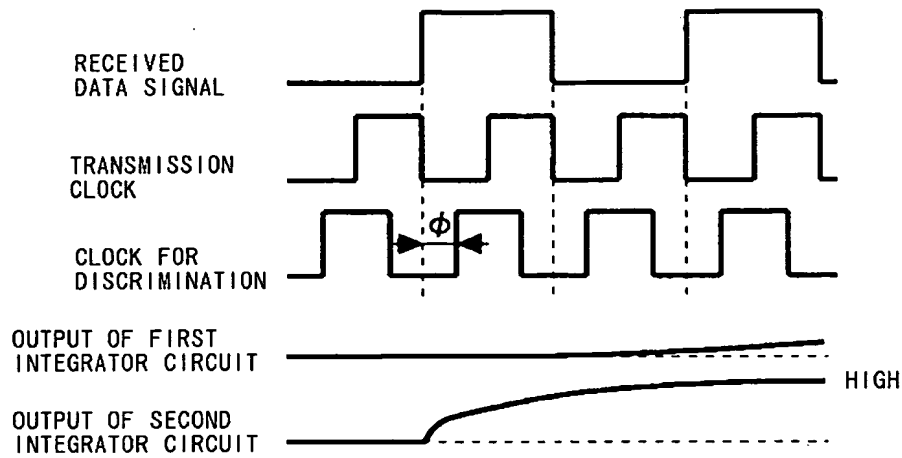


FIG. 5B CASE OF PHASE LAG OF RECEIVED DATA SIGNAL

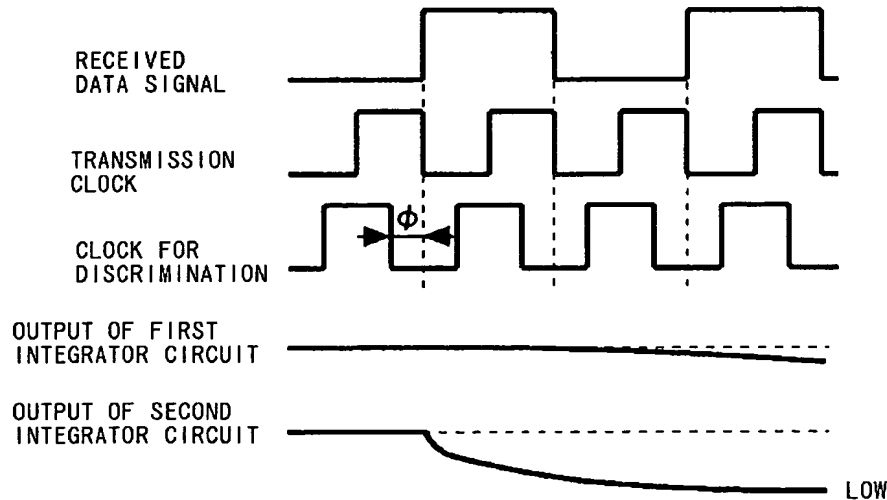


FIG. 5C CASE OF SYNCHRONIZATION OF RECEIVED DATA SIGNAL WITH CLOCK FOR DISCRIMINATION

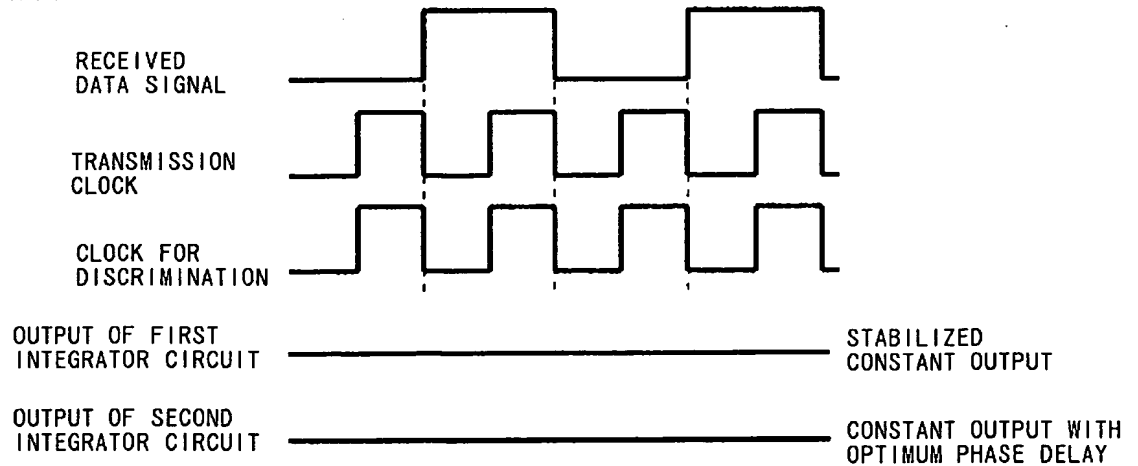
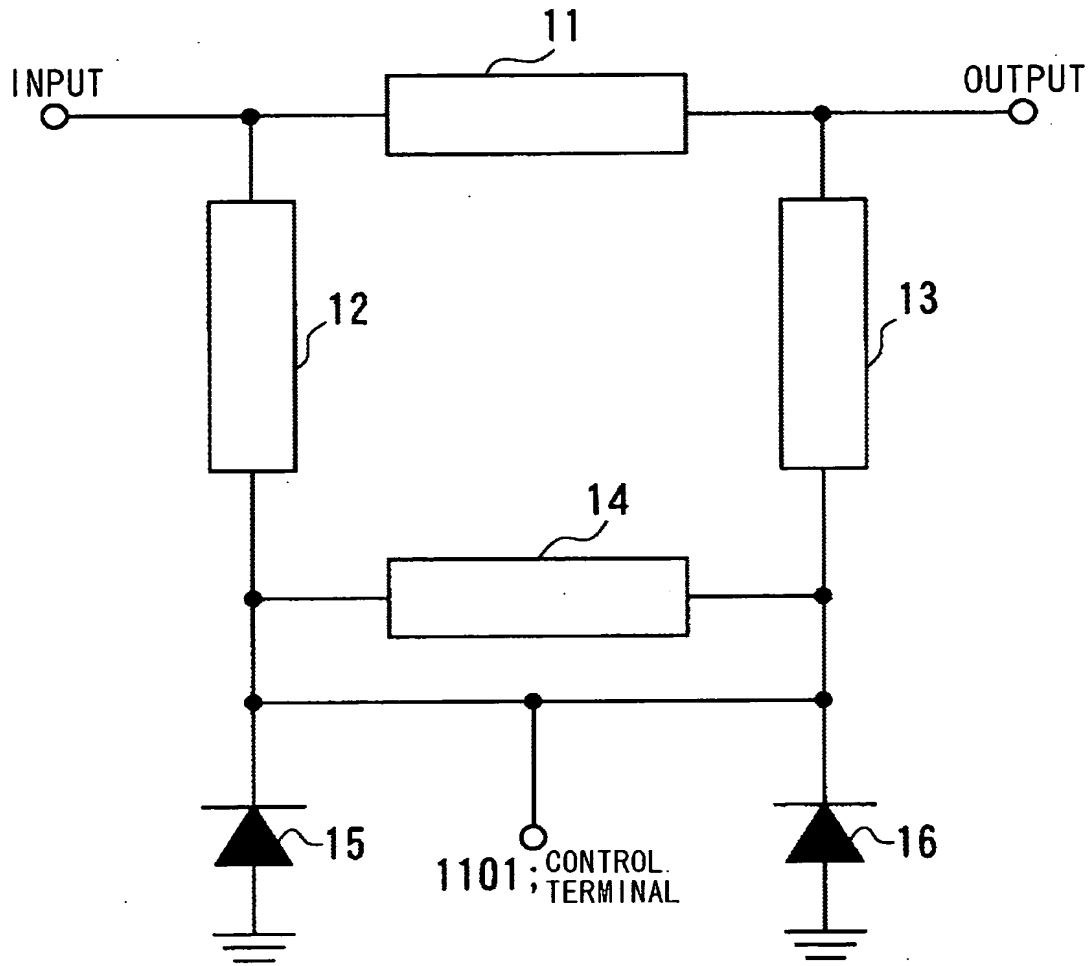


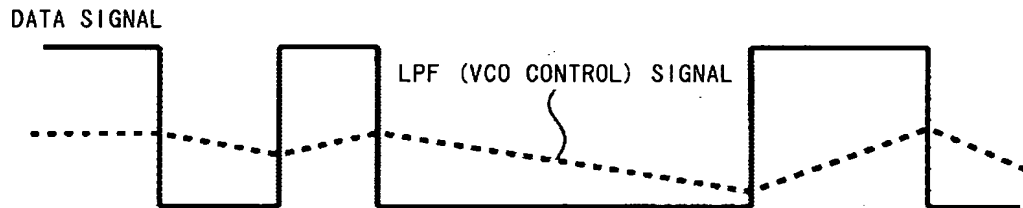
FIG . 6



PHASE SHIFTER

FIG . 7A

CASE OF SHORT TIME CONSTANT OF LPF

**FIG . 7B**

CASE OF LONG TIME CONSTANT OF LPF

**FIG . 7C**

CASE OF LONG TIME CONSTANT OF LPF1 AND SHORT TIME CONSTANT OF LPF2

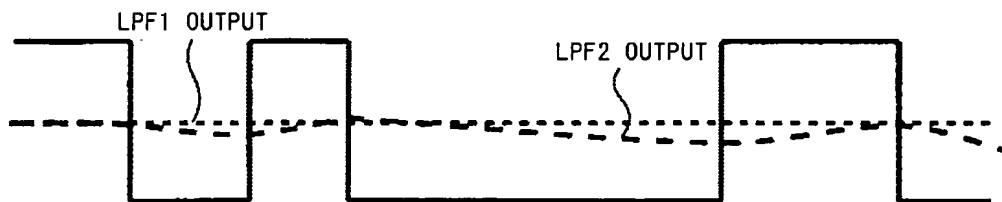


FIG . 8 PRIOR ART

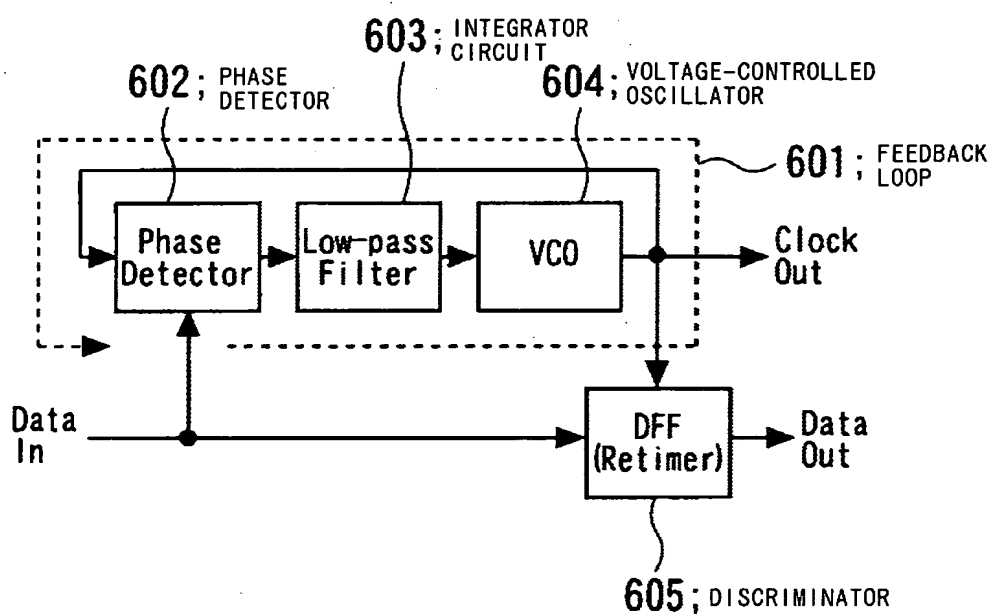


FIG . 9

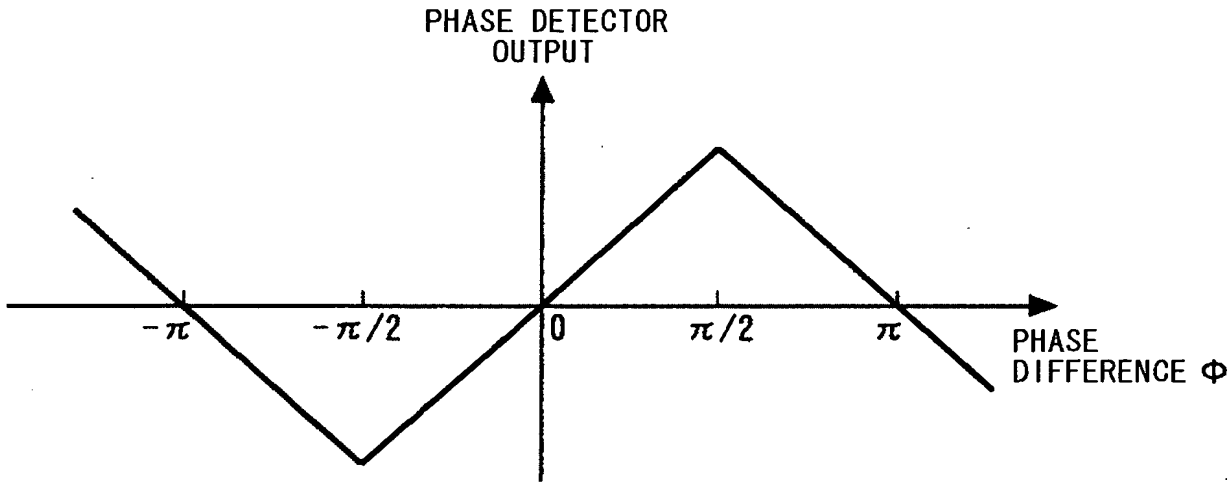


FIG . 10A PRIOR ART

CASE OF PHASE LEAD OF CLOCK SIGNAL FOR DISCRIMINATION

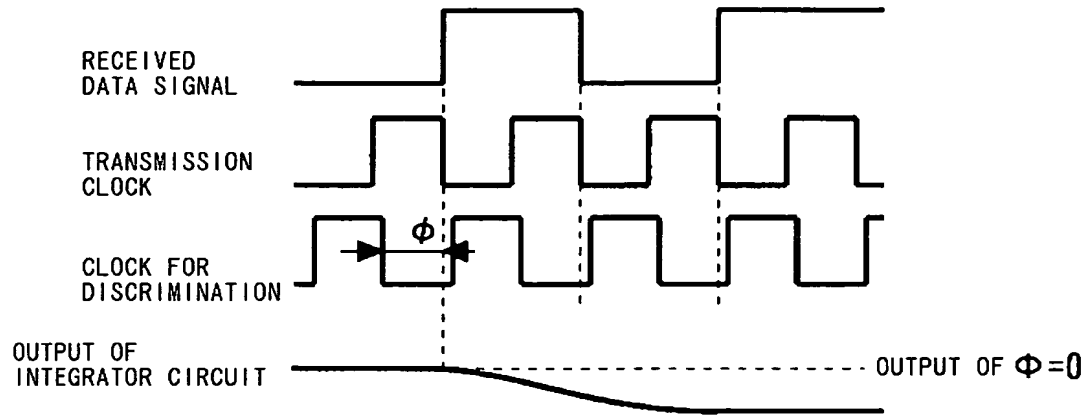


FIG . 10B PRIOR ART

CASE OF PHASE LAG OF CLOCK SIGNAL FOR DISCRIMINATION

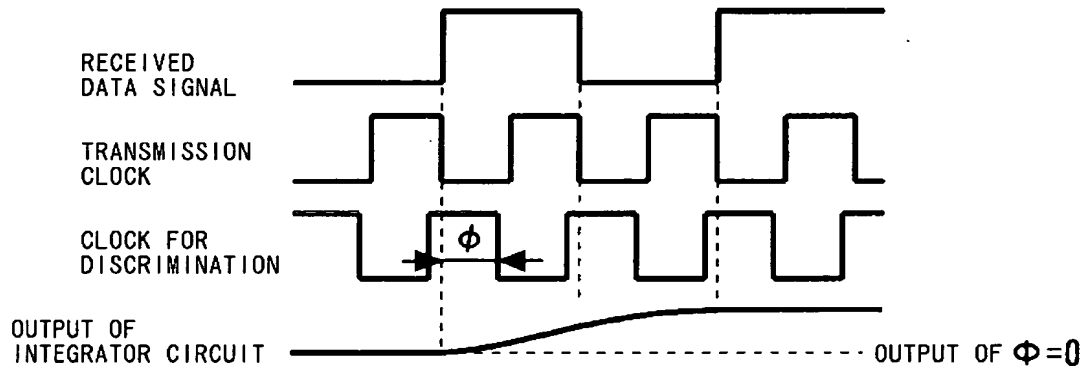


FIG . 10C PRIOR ART

CASE OF SYNCHRONIZATION OF CLOCK SIGNAL FOR DISCRIMINATION

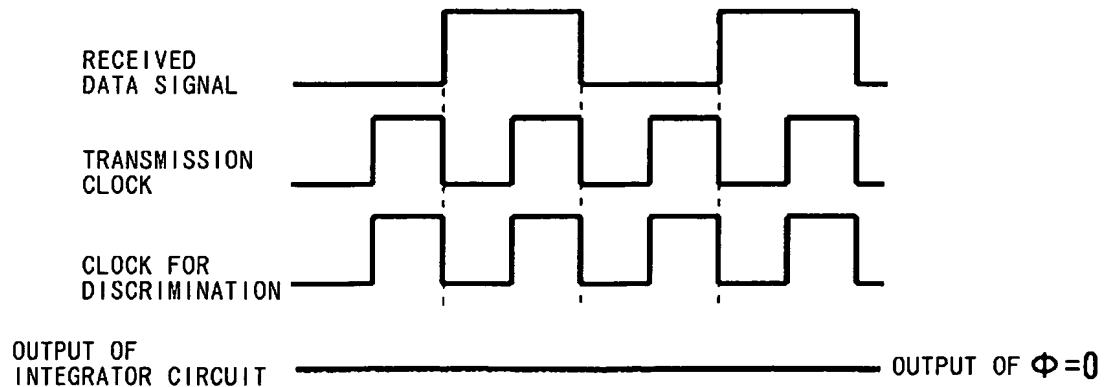


FIG. 11 PRIOR ART

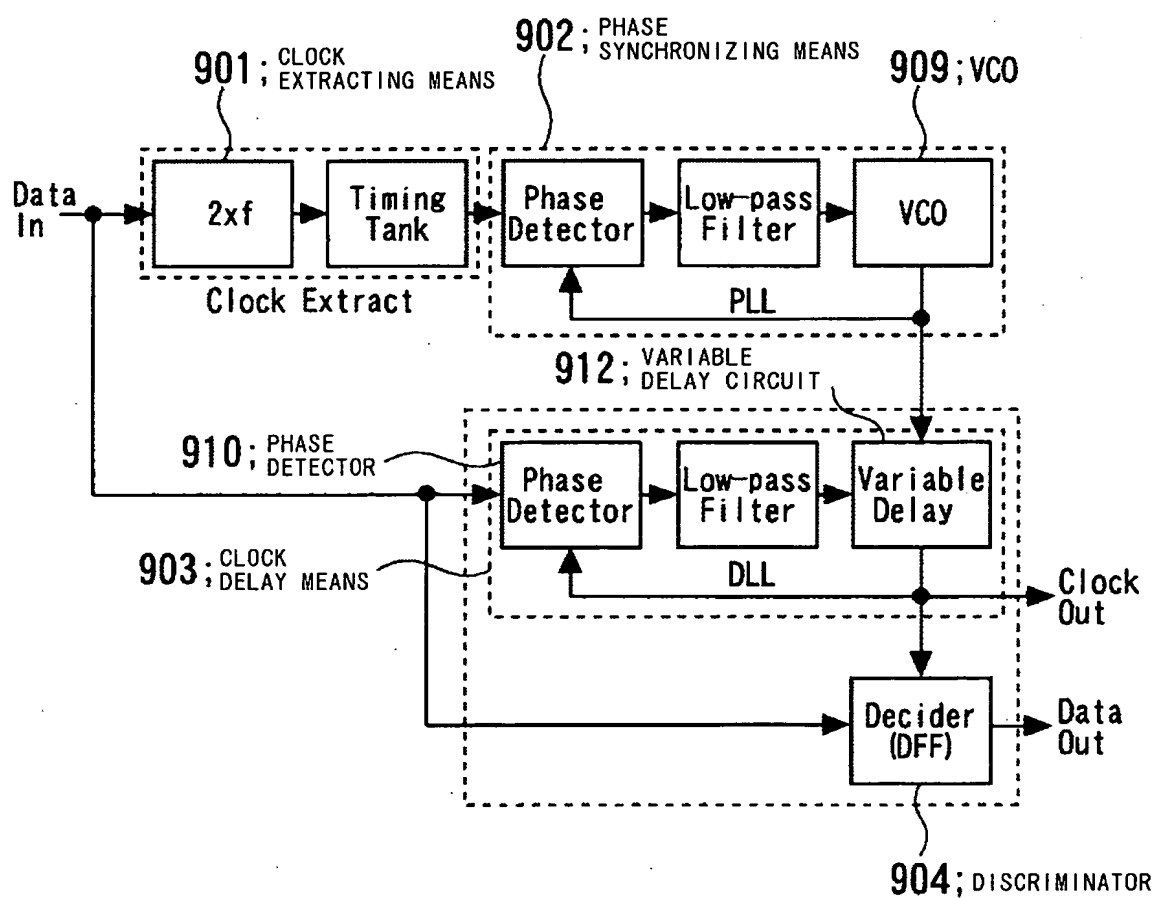


FIG . 12A

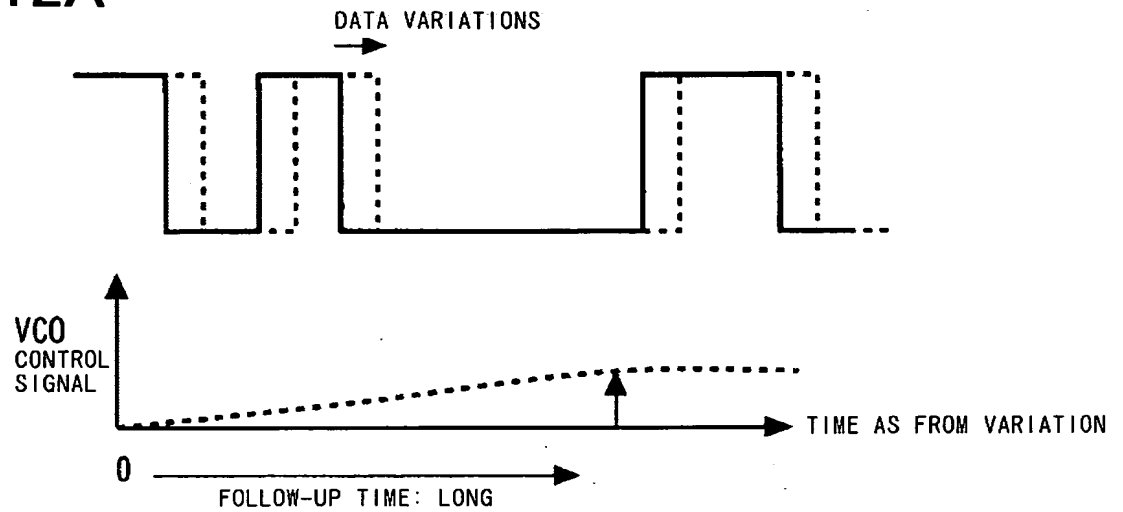


FIG . 12B

CASE OF LONG TIME CONSTANT OF INTEGRATING CIRCUIT (LPF)

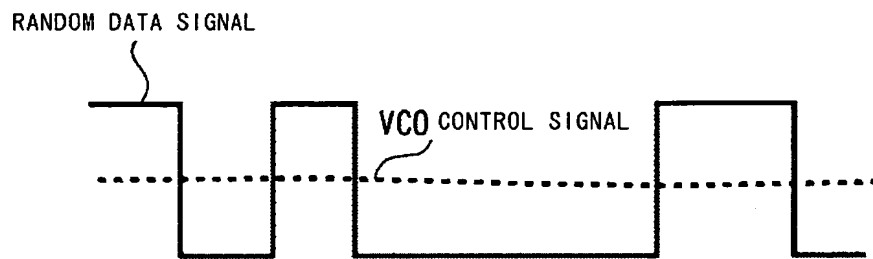


FIG . 12C

CASE OF SHORT TIME CONSTANT OF INTEGRATING CIRCUIT (LPF)

